



## Form PTO-1449 (modified)

Atty. Docket No.  
SILA:097Serial No.  
10/075,099

List of Patents and Publications for Applicant's

Applicants  
TOD PAULUS ET AL.

## INFORMATION DISCLOSURE STATEMENT

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Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
J.L.	A1	5,828,955	10/27/98	Lipowski et al.			8/30/95
J.L.	A2	6,035,186	3/7/00	Moore et al.			3/11/97
J.L.	A3	6,075,979	6/13/00	Holtvoeth et al.			3/5/97
J.L.	A4	5,764,171	6/9/98	Stikvoort			4/2/96
J.L.	A5	6,148,048	11/14/00	Kerth et al.			9/26/97
J.L.	A6	4,713,563	12/15/87	Marshall et al.			5/12/86
J.L.	A7	4,070,632	1/24/78	Turtle			9/22/76
J.L.	A8	4,236,252	11/25/80	Kominami et al.			2/6/79
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J.L.	A10	4,857,928	8/15/89	Gailus et al.			1/28/88
J.L.	A11	4,989,074	1/29/91	Matsumoto			9/21/89
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J.L.	A16	5,235,410	8/10/93	Hurley			7/10/91
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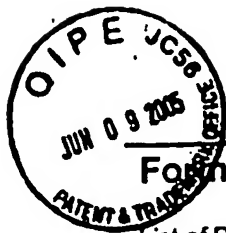
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J.L	A26	5,742,189	4/21/98	Yoshida et al.			9/14/95
J.L	A27	5,862,465	1/19/99	Ou			12/30/96
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J.L.	B3	0643477A2	3/15/95	Hulkko et al.			
J.L.	B4	WO 00/11794	3/2/00	Moore et al.			
J.L.	B5	WO 00/01074	1/6/00	Van Der Zwan et al.			
J.L.	B6	WO 99/22456	5/6/99	Grenabo			10/27/98

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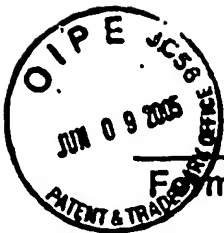
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J.L.	C1	Stephen Jantzi et al., "Quadrature Bandpass $\Delta\Sigma$ Modulation for Digital Radio," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 1935-1950.
J.L.	C2	Stephen Jantzi et al., "A Complex Bandpass $\Delta\Sigma$ Converter For Digital Radio," ISCAS, May/June 1994, pp. 453-456.
J.L.	C3	"Analog Devices Delivers World's First Open Market GSM Direct Conversion Radio Chipset," Analog Devices Corporate Information Press Release, <a href="http://contentanalog.com/pressrelease/prdisplay/0,1622,102,00.html">http://contentanalog.com/pressrelease/prdisplay/0,1622,102,00.html</a> , September 13, 1999, pp. 1-4.

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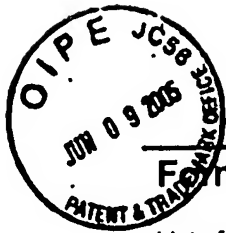
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J.h	C5	Jacques C. Rudell et al., "A 1.9-GHz Wide-Band IF Double Conversion CMOS Receiver for Cordless Telephone Applications," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 2071-2088.
J.h	C6	Jan Crols et al., "Low-IF Topologies for High-Performance Analog Front Ends of Fully Integrated Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pp. 269-282.
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J.h	C10	Stephen Jantzi et al., "FP 13.5: A Quadrature Bandpass $\Delta\Sigma$ Modulator for Digital Radio," Digest of Technical Papers, 1997 IEEE International Solid-State Circuits Conference, First Edition, February 1997, pp. 216-217, 460.
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J.h	C17	Jan Crols et al., "A Single-Chip 900 MHz CMOS Receiver Front-End With A High Performance Low-IF Topolgy," IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, pp. 1483-1492.
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J.h	C19	Analog Devices, AD6523/AD6524, GSM Direct Conversion Radio Chip Set, <a href="http://www.analog.com">www.analog.com</a> , 2 pgs.
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J.h	C62	Shoaei et al., "Optimal (Bandpass) Continuous-Time $\Delta\Sigma$ Modulator," pp. 489-492.
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J.h	C71	Rudell, et al., "Second Generation Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 9 (June 1996)
J.h	C72	Cho, et al., "Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 26 (June 1996)
J.h	C73	Copending U.S. Patent Application Serial No. 09/821,342, filed March 29, 2001, "Partitioned Radio-Frequency Apparatus And Associated Method" (SILA:072)
J.h	C74	Copending U.S. Patent Application Serial No. 09/821,340, filed March 29, 2001, "Digital Interface In Radio-Frequency Apparatus And Associated Methods" (SILA:073)

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Atty. Docket No.  
SILA:097Serial No.  
10/075,099

List of Patents and Publications for Applicant's

Applicants  
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## INFORMATION DISCLOSURE STATEMENT

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2681U.S. Patent Documents  
See Pages 1-3Foreign Patent Documents  
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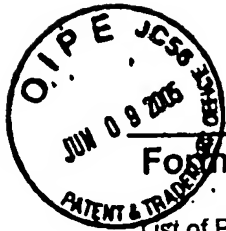
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